

## REMARKS

This Amendment responds to the Office Action dated August 26, 2004 in which the Examiner rejected claims 1-2 and 8 under 35 U.S.C. §102(b) and rejected claims 4 and 6-7 under 35 U.S.C. §103.

As indicated above, claim 1 has been amended to make explicit what is implicit in the claim. Claim 7 has been amended to be consistent with claim 1. The amendment is unrelated to a statutory requirement for patentability.

Claim 1 claims a system for monitoring wavelength division multiplex channels in an optical signal. The system comprises a phase-array optical wavelength demultiplexer device, a phase control means, a detector and control means. The phase-array optical wavelength demultiplexer (phasar) device includes an input port for receiving an input optical signal, and an output port for transmitting an optical signal. The input and output ports are connected by a waveguide array. The phase control means is connected to receive a control signal and is operable to vary the effective optical length of each waveguide in the array, and such that the phase of optical signals passing through respective waveguides also vary in dependence upon that received control signal. The detector is connected to receive the output optical signal from the phasar device, and is operable to produce a detector signal relating to that output optical signal. The control means is connected to receive the detector signal, and is operable to supply the phase control means such that a selected signal relating to a desired one of the multiplexed channels is output from the phasar device to the detector. The control means also is operable to vary the control signal such that a plurality of such selected signals, relating to

respective multiplexed channels, are output in turn from the phasar device to the detector.

Through the structure of the claimed invention having a) a detector to receive an output optical signal from a phasar device b) a control means which supplies the phase control means such that a plurality of selected signals are output in turn from the phasar device to the detector as claimed in claim 1, the claimed invention provides a system for monitoring wavelength division multiplex channels in an optical signal so that the wavelength of light going to a given output changes over a predetermined range. The prior art does not show, teach or suggest the invention as claimed in claim 1.

Claims 1-2 and 8 were rejected under 35 U.S.C. §102(b) as being anticipated by *Koga et al.* (U.S. Patent No. 5,617,234).

*Koga et al.* appears to disclose a multiwavelength simultaneous monitoring circuit employing arrayed-waveguide grating preferably used as a wavelength meter in optical communication networks using wavelength division multiplexing (WDM) technologies, or as a wavelength discriminator in a stabilizing circuit for wavelength division multiplexing optical sources. (col. 1, lines 8-14) FIG. 4 shows a configuration of a first embodiment of a multiwavelength simultaneous monitoring circuit. In this figure, a reference optical signal (wavelength  $\lambda_0$ ) and a WDM signal to be monitored (wavelength  $\lambda_1 - \lambda_n$ ) are inputted to a predetermined input waveguide of an AWG (Arrayed-Waveguide Grating) 12 after multiplexed by an optical coupler 11. The AWG 12 includes the following elements which are connected in this order: an input waveguide array 32 formed on a substrate 31; an input concave-slab waveguide 33; a waveguide array 34 including a plurality of waveguides which

progressively become longer by a length difference  $\Delta L$ , an output concave-slab waveguide 35; and an output waveguide array 36. The waveguide array 34 is provided with a heater 13 connected to a current source 15. The current source 15 is supplied with a reference signal  $S_a$  outputted from an oscillator 14, and controls the temperature of the heater 13 in response to the reference signal  $S_a$ . The output waveguides #0-#n of the AWG 12 are connected to photodetectors 16-0 and 16-i ( $i=1-n$ ) as shown in FIG. 5A. The outputs of the photodetectors 16-0 and 16-i are connected to phase comparators 18-0 and 18-i through amplifiers 17-0 and 17-i, respectively. The phase comparators 18-0 and 18-i are provided with the reference signal  $S_a$  outputted from the oscillator 14, and their outputs are inputted to low-pass filters (LPFs) 19-0 and 19-i. The output of the low-pass filter 19-0 is supplied to an integrator 20-0. The output (wavelength error signal  $S_d$ ) of the integrator 20-0 is fed to a temperature control circuit 21 that controls a Peltier cooler 22 for regulating the temperature of the AWG 12. (col. 6, line 38 through col. 7, line 2) Since the transmission characteristics of the AWG 12 is locked to the reference wavelength  $\lambda_0$ , error signals detected by the coherent detection in the phase comparators 18-1-18-n correspond to the relative wavelength differences between the respective wavelengths  $\lambda_1-\lambda_n$  of the WDM signal and the center transmission wavelengths of the output waveguides #1-#n. (col. 9, lines 15-21) Thus, the multiwavelength simultaneous monitoring circuit of this embodiment can discriminate the wavelength errors of the WDM signal to be monitored at high accuracy by utilizing the highly accurate relationships between the center transmission wavelengths of the output waveguides of the AWG 12, and by locking one of the center transmission wavelengths to the reference wavelength  $\lambda_0$ . (col. 9, lines 42-48) FIGS. 22 and 23

are block diagrams showing a fifth embodiment of the multiwavelength simultaneous monitoring circuit. The fifth embodiment differs from the third embodiment shown in FIG. 14A in that the present embodiment obtains ratios between the outputs of two adjacent output ports rather than the differences between the two outputs as in the third embodiment. To achieve this in the present embodiment, the outputs of a pair of photodetectors 16-ia and 16-ib are supplied to the first and second input terminals of logarithmic amplifiers 40-i, respectively. Each of the logarithmic amplifiers 40-i converts the two input signals into log values, followed by obtaining the difference between the two, thereby outputting the ratio of the outputs of the pair of adjacent output ports as the wavelength error signal. The AWG 12 of the present embodiment is a 16.times.16 AWG, and the wavelength spacing between the center transmission wavelengths of adjacent channels is 1 nm. Accordingly, FSR (Free Spectral Range) is 16 nm. The input ports are assigned to eight central input ports of the AWG 12. The center transmission wavelengths of respective channels of the AWG 12 vary in accordance with the position of an input port of the multiplexed light of the WDM signal and the reference optical signal. (col. 14, lines 13-37)

Thus, *Koga et al.* merely discloses a photodetector provided for each individual signal (col. 6, lines 57-59). However, as claimed in claim 1, a detector receives an output optical signal and a control means supplies a plurality of selected signals in turn to the detector (i.e. signals are scanned to a single detector). However, *Koga et al.* teaches away from the claimed invention since one signal is provided to each photodetector 16-0 through 16-I (i.e. *Koga et al.* has plural detectors and no scanning).

Since nothing in *Koga et al.* shows, teaches or suggests a control means supplies a plurality of selected signals in turn to a detector as claimed in claim 1, Applicant respectfully requests the Examiner withdraws the rejection to claim 1 under 35 U.S.C. §102(b).

Claims 2 and 8 depend from claim 1 and recite additional features. Applicant respectfully submits that claims 2 and 8 would not have been anticipated by *Koga et al.* within the meaning of 35 U.S.C. §102(b) at least for the reasons as set forth above. Therefore, Applicant respectfully requests the Examiner withdraws the rejection to claims 2 and 8 under 35 U.S.C. §102(b).

Claim 4 were rejected under 35 U.S.C. §103 as being unpatentable over *Koga et al.* in view of *Podoleanu et al.* (U.S. Patent No. 5,975,697). In addition, claims 6-7 were rejected under 35 U.S.C. §103 as being unpatentable over *Koga et al.* in view of *Weber* (PCT WO99/12297).

Applicant respectfully traverses the Examiner's rejection of the claims under 35 U.S.C. §103. The claims have been reviewed in light of the Office Action, and for reasons which will be set forth below, Applicant respectfully requests the Examiner withdraws the rejection to the claims and allows the claims to issue.

As discussed above, since nothing in *Koga et al.* shows, teaches or suggests the primary features as claimed in claim 1, Applicant respectfully submits that the combination of the primary reference with the secondary references will not overcome the deficiencies of the primary reference. Therefore, Applicant respectfully requests the Examiner withdraws the rejection to claims 4 and 6-7 under 35 U.S.C. §103.

Thus, it now appears that the application is in condition for reconsideration and allowance. Reconsideration and allowance at an early date are respectfully requested. Should the examiner find that the application is not now in condition for allowance, Applicant respectfully requests the Examiner enters this Amendment for purposes of appeal.

If for any reason the Examiner feels that the application is not now in condition for allowance, the Examiner is requested contact, by telephone, the Applicant's undersigned attorney at the indicated telephone number to arrange for an interview to expedite the disposition of this case.

In the event that this paper is not timely filed within the current set shortened statutory period, Applicant respectfully petitions for an appropriate extension of time. The fees for such extension of time may be charged to our Deposit Account No. 02-4800.

In the event that any additional fees are due with this paper, please charge our Deposit Account No. 02-4800.

Respectfully submitted,

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